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NOTICE OF ALLOWANCE AND FEE(S) DUE

2292 7590 0223/2010 BIRCH STEWART KOLASCH & BIRCH PO BOX 747 FALLS CHURCH, VA 22040-0747 EXAMINER

PATEL, JAYESH A

ART UNIT PAPER NUMBER

2624

DATE MAILED: 02/23/2010

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/578,686	05/09/2006	Satoshi Yamanaka	1190-0625PUS1	3665

TITLE OF INVENTION: MEAN PRESERVING INTERPOLATION CALCULATION CIRCUIT, PIXEL INTERPOLATION CIRCUIT, MEAN PRESERVING INTERPOLATION METHOD, AND PIXEL INTERPOLATION METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	05/24/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 1SI. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FFE: shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)					Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.				
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BIRCH STEWART KOLASCH & BIRCH PO BOX 747 FALLS CHURCH, VA 22040-0747				I b St: ad tra	Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSEE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.				
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								(D	tate)
APPLICATION NO.	FILING DATE			FIRST NAMED INVENTO	R	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.	П
10/578,686	05/09/2006			Satoshi Yamanaka		1	190-0625PUS1	3665	_
TITLE OF INVENTION PRESERVING INTERPO	DLATION METHOD, A	ND PL	XEL INTERPOLA	ATION METHOD					
APPLN, TYPE	SMALL ENTITY	ISS	SUE FEE DUE	PUBLICATION FEE DUE		E FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO		\$1510	\$300	\$0		\$1810	05/24/2010	
EXAMI	INER		ART UNIT	CLASS-SUBCLASS					
PATEL, JA			2624	382-300000	_				
"Fee Address" indi- PTO/SB/47; Rev 03-0. Number is required. 3. ASSIGNEE NAME AN	ondence address (or Cha /122) attached. cation (or "Fee Address 2 or more recent) attach	nge of G Indica ed. Use	Correspondence tion form of a Customer E PRINTED ON T		o 3 registered pater ively, gle firm (having as a agent) and the nam orneys or agents. If e printed.	memb es of u no nan	era 2pto	ocument has been filed	
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	SMALL ENTITY state	ıs. See :	37 CFR 1.27.	☐ b. Applicant is no lo					
NOTE: The Issue Fee and interest as shown by the re	I Publication Fee (if req ecords of the United Sta	uired) v tes Pate	rill not be accepted int and Trademark	d from anyone other than Office.	the applicant; a regi	stered	attorney or agent; or th	e assignee or other part	/ in
Authorized Signature					Date				
Typed or printed name				Registration No.					
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/578,686 05/09/2006		Satoshi Yamanaka	1190-0625PUS1	3665
2292 75	90 02/23/2010	EXAMINER		
BIRCH STEWA	RT KOLASCH & BI	PATEL, JAYESH A		
PO BOX 747		ART UNIT	PAPER NUMBER	
FALLS CHURCH	, VA 22040-0747	2624		

DATE MAILED: 02/23/2010

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 802 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 802 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Application No. Applicant(s) 10/578 686 YAMANAKA ET AL. Notice of Allowability Examiner Art Unit JAYESH PATEL 2624 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. This communication is responsive to Amendments/remarks filed on 12/10/2009. 2. The allowed claim(s) is/are 1-20. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). b) ☐ Some* c) ☐ None of the: 1. A Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). * Certified copies not received: _____. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s) 1. Notice of References Cited (PTO-892) 5. Notice of Informal Patent Application 2. Notice of Draftperson's Patent Drawing Review (PTO-948) Interview Summary (PTO-413), Paper No./Mail Date 3. Information Disclosure Statements (PTO/SB/08), 7. X Examiner's Amendment/Comment Paper No./Mail Date 4. T Examiner's Comment Regarding Requirement for Deposit 8. X Examiner's Statement of Reasons for Allowance of Biological Material Other .

/JAYESH PATEL/ Patent Examiner, Art Unit 2624 Art Unit: 2624

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Ali M Imam. (Reg # 58755) on Jan 27 2010.

The application has been amended as follows:

Claim 1. (Currently Amended) A mean preserving interpolation calculation circuit that obtains interpolation data for a missing pixel such that a mean value of a sequence of pixels constituting a group of pixels including the missing pixel is equal to a mean value of another sequence of pixels constituting a group of pixels not including the missing pixel, wherein the number of pixels constituting the group of pixels including the missing pixel and the number of pixels constituting the group of pixels not including the missing pixel are [[both]] each k, [[(1)] k being a positive integer [[)]].

Claim 5. (Currently Amended) The mean preserving interpolation calculation circuit of claim 2, wherein each said group of pixels constitutes a portion of a periodically varying series of pixels, i pixels, [[(]] i being a positive integer [[()]] in the pixels constituting the group of pixels not including the missing pixel coincide

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with i pixels in the pixels constituting the group of pixels including the missing pixel, and said k or a value obtained by subtracting said i from said k is set to a value substantially equal to a period of pixel variation represented by a number of pixels, or to an integer multiple thereof.

Claim 6. (Currently Amended) A pixel interpolation circuit comprising; [[the mean preserving interpolation calculation circuit of claim 1]] a mean preserving interpolation calculation circuit that obtains interpolation data for a missing pixel such that a mean value of a sequence of pixels constituting a group of pixels including the missing pixel is equal to a mean value of another sequence of pixels constituting a group of pixels not including the missing pixel, wherein the number of pixels constituting the group of pixels including the missing pixel and the number of pixels constituting the group of pixels not including the missing pixel and the number of pixels constituting the group of pixels not including the missing pixel are each k, k being a positive integer; and a circuit using maximum and minimum values of pixels in a neighborhood of the missing pixel to limit the output range of the interpolation data obtained by the mean preserving interpolation calculation circuit.

Claim 7. (Currently Amended) A pixel interpolation circuit comprising: a plurality of mean preserving interpolation calculation circuits [[as recited in claim 5,]] each obtaining interpolation data for a missing pixel such that a mean value of a sequence of pixels constituting a group of pixels including the missing pixel

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is equal to a mean value of another sequence of pixels constituting a group of pixels not including the missing pixel, wherein the number of pixels constituting the group of pixels including the missing pixel and the number of pixels constituting the group of pixels not including the missing pixel are each k, k being a positive integer, wherein each of the plurality of mean preserving interpolation calculation circuits comprises:

an incomplete total calculation circuit for obtaining a total of values of the pixels other than the missing pixel in the k pixels constituting the group of pixels including the missing pixel:

a complete total calculation circuit for obtaining a total of values of the k pixels constituting the group of pixels not including the missing pixel; and

a difference circuit for obtaining interpolation data for the missing pixel by subtracting an output of the complete total calculation circuit from an output of the incomplete total calculation circuit, wherein

each said group of pixels constitutes a portion of a periodically varying series of pixels, i pixels, i being a positive integer, in the pixels constituting the group of pixels not including the missing pixel coincide with i pixels in the pixels constituting the group of pixels including the missing pixel, and said k or a value obtained by subtracting said i from said k is set to a value substantially equal to a period of pixel variation represented by a number of pixels, or to an integer multiple thereof, and wherein each of the mean preserving interpolation calculation circuits have different values of said k or (k - i), and wherein the pixel

interpolation circuit further comprising an output circuit, the plurality of mean preserving interpolation calculation circuits generating respective interpolation data for the missing pixel, the output circuit selecting one of the interpolation data output by the plurality of mean preserving interpolation calculation circuits and outputting the selected data as the interpolation data for the missing pixel.

Claim 13. (Currently Amended) A mean preserving interpolation calculation method that obtains interpolation data for a missing pixel such that a mean value of a sequence of pixels constituting a group of pixels including the missing pixel is equal to a mean value of another sequence of pixels constituting a group of pixels not including the missing pixel, wherein the number of pixels constituting the group of pixels including the missing pixel and the number of pixels constituting the group of pixels not including the missing pixel are [[both]] each k, [[(I]] k being a positive integer [[[)]].

Claim 17. (Currently Amended) The mean preserving interpolation calculation method of claim 14, wherein each said group of pixels constitutes a portion of a periodically varying series of pixels i pixels. [[(]] i being a positive integer [[)]] in the pixels constituting the group of pixels not including the missing pixel coincide with i pixels in the pixels constituting the group of pixels including the missing pixel, and said k or a value obtained by subtracting said i from said k

is set to a value substantially equal to a period of pixel variation represented by a number of pixels, or to an integer multiple thereof.

Claim 18. (Currently Amended) A pixel interpolation method comprising:

a mean preserving interpolation calculation step that obtains interpolation data
for a missing pixel such that a mean value of a sequence of pixels constituting a
group of pixels including the missing pixel is equal to a mean value of another
sequence of pixels constituting a group of pixels not including the missing pixel,
wherein the number of pixels constituting the group of pixels including the
missing pixel and the number of pixels constituting the group of pixels not
including the missing pixel are each k, k being a positive integer; and a
[[further]] step of using maximum and minimum values of pixels in a
neighborhood of the missing pixel to limit the output range of the interpolation
data obtained by the mean preserving interpolation calculation [[method of
claim 13]] step.

Claim 19. (Currently Amended) A pixel interpolation method <u>comprising</u>:

a plurality of mean preserving interpolation calculation steps each obtaining interpolation data for a missing pixel such that a mean value of a sequence of pixels constituting a group of pixels including the missing pixel is equal to a mean value of another sequence of pixels constituting a group of pixels not including the missing pixel, wherein the number of pixels constituting the group of pixels

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including the missing pixel and the number of pixels constituting the group of pixels not including the missing pixel are each k, k being a positive integer: wherein each of the mean preserving interpolation calculation steps comprises: an incomplete total calculation step for obtaining a total of values of the pixels other than the missing pixel in the k pixels constituting the group of pixels including the missing pixel; a complete total calculation step for obtaining a total of values of the k pixels constituting the group of pixels not including the missing pixel; and a difference step for obtaining interpolation data for the missing pixel by subtracting an output of the complete total calculation step from an output of the incomplete total calculation step, wherein each said group of pixels constitutes a portion of a periodically varying series of pixels, i pixels, i being a positive integer, in the pixels constituting the group of pixels not including the missing pixel coincide with i pixels in the pixels constituting the group of pixels including the missing pixel, and said k or a value obtained by subtracting said i from said k is set to a value substantially equal to a period of pixel variation represented by a number of pixels, or to an integer multiple thereof; and wherein each of the mean preserving interpolation calculation step [[of claim 17]] is performed with different values of said k or (k - i), each time generating interpolation data for the missing pixel, and the output step selects one of the interpolation data output by the plurality of mean preserving interpolation calculation steps and outputs the selected data as the interpolation data for the missing pixel.

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REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: In view of the amendments/remarks dated 12/10/2009 and the prior arts cited on PTO 892, Independent claims 1,6,7,13, 18-19 are allowed. Claims 2-5,8-12, 14-17 and 20 depends directly or indirectly on the independent claims1,6,7,13, 18-19 and therefore they are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAYESH PATEL whose telephone number is (571)270-1227. The examiner can normally be reached on 5-4-9.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Samir Ahmed can be reached on 571-272-7413. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-

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direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

2/4/2010

/JAYESH PATEL/ Examiner, Art Unit 2624

/Samir A. Ahmed/ Supervisory Patent Examiner, Art Unit 2624